Amendments to the Claims

These claims will replace all prior versions, and listings, of claims in the application:

1 - 10. (Canceled)

11. (currently amended) A timing recovery loop in the front end of a digital receiver including N antennae, comprising:

N sample rate converters, each receiving an Nth symbol stream at a first sampling rate from an Nth antenna and outputting the Nth symbol stream at a second sampling rate responsive to a timing recovery (TR) control signal;

N forward equalizers, each generating an Nth equalized feedback signal based on the Nth symbol stream at the second sampling rate, respectively;

a timing recovery circuit generating the TR control signal based upon a selected one of the N equalized feedback signals; and

N carrier recovery circuits, each electrically coupling an Nth one of the N sample rate converters to an Nth one of the forward equalizers, where N is equal to or greater than 2.

12. (Canceled)

13. (Previously presented) The timing recovery loop as recited in claim 11, further comprising N finite impulse response (FIR) filters, each electrically coupling an Nth one of the carrier recovery circuits to an Nth one of the forward equalizers.

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14. (Original) The timing recovery loop as recited in claim 13, wherein each of the N FIR filters is a square-root raised cosine filter.

15. (Original) The timing recovery loop as recited in claim 11, further comprising a selector receiving N signals based on the N equalized feedback signals at N respective input terminals and applying the selected one of the N signals to the timing recovery circuit.

16. (Canceled)

17. (currently amended) A method for operating a digital receiver, including N sample rate converters responsive to a timing recovery (TR) control signal, connected to N antennae, respectively, comprising:

      generating and outputting N equalized feedback signals, each based on an Nth symbol stream having a controlled sample rate;

      combining the N equalized feedback signals to produce a combined equalized feedback signal;

      producing the TR control signal based on the combined equalized feedback signal; and

      applying the TR control signal to the sample rate converters to thereby permit the N sample rate converters to output N symbol streams at the controlled sample rate, where N is equal to or greater than 2.

18. (Canceled)

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